

**UNIVERSITY COLLEGE OF ENGINEERING  
OSMANIA UNIVERSITY, HYDERABAD - 7**

**M. E. (ECE, Mechanical & CSE) II - Semesters (Main) Examination, August 2011**

<b>Examination Centre: Main Building UCE (A), O.U.</b>
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**M V S R ENGINEERING COLLEGE  
EXAMINATION TIME-TABLE**

**Time : 2.00 PM to 5.00 PM**

*“Centre : University College of Engineering, Osmania University, Hyderabad – 7”*

DATE & DAY	E. C. E. (EMBEDDED SYSTEMS & VLSI DESIGN)	MECHANICAL (CAD / CAM)	C. S. E.
	II-Semester	II-Semester	II-Semester
16-08-2011 TUESDAY	VLSI Physical Design	Failure Analysis & Design	Software Quality and Testing
18-08-2011 THURSDAY	Principles of VLSI System Design	Computer Aided Mechanical Design & Analysis	Advanced Databases
20-08-2011 SATURDAY	Low Power VLSI Design	Rapid Prototyping Principles & Applications	Grid Computing
22-08-2011 MONDAY	Scripting Languages for VLSI Design Automation	Automation	Distributed Computing
24-08-2011 WEDNESDAY	Real Time Operating Systems	Vibration Analysis & Condition Monitoring	Software Project Management
26-08-2011 FRIDAY	Design for Testability	Design of Dies	Image Processing

**Note: This Time-Table is also applicable for Pre-Ph.D. students registered for August 2011 Examination.**

DIRECTOR OF EVALUATION  
EXAMINATION CELL